

SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION							SERIAL NO 10/532,9	rial no. 0/532,993		
			•		APPLICANT Michihito UEDA					
		(PT	O-1449)		FILING DATE April 28, 2005	GROUP Not yet a	OUP ot yet assigned			
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.



SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION						ATTY. DOCKET NO. 061352-0100					
						APPLICANT Michihito UEDA					
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		YAMANAKA et al.; "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals"; IEICE Trans. Electron.; c. 2001; Vol. E84, No. 12; pp. 1723 - 1729									
											
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